

**AMENDMENTS TO THE SPECIFICATION:**

Please replace the paragraph beginning at line 7 on page 7 as follows:

The access control to each of the memory modules **13**, **14**, and **15** from the memory controller **121** can be performed through the memory bus **4**. The memory bus **4** in the computer system **10** is designated to operate at an established operating frequency, for example, of 100 MHz, ~~by a frequency controller 122 included in memory controller 121.~~ Of course, it is possible to change the designated operating frequency for the memory bus **4**. In order to change the designated operating frequency for the memory bus **4**, the memory controller **121** selects a desired clock signal that is generated from a clock generator (not shown) for operating the memory bus **4**.